

DC-DC CONVERTER 3W, SIP Package

## **FEATURES**

- ► Industrial Standard SIP-7 Package
- ► Semi-regulated Output Voltage
- ► Very High Efficiency up to 89%
- ► High I/O Isolation 1000VDC
- ▶ Operating Ambient Temp. Range -40°C to +95°C
- ► UL/cUL/IEC/EN 62368-1(60950-1) Safety Approval















# **PRODUCT OVERVIEW**

The MINMAX MA03 series is a range of isolated 3W DC-DC converter modules in a small SIP-package. There are 12 models available with 5V, 12V or 24VDC input. These products have a typical load regulation of 5.0% to 7.0% depending on model.

The MA03 DC-DC converters are a compromise between a more expensive fully regulated converter and a non-regulated converter. They offer the designer a solution for many cost critical applications where the output voltage variation has to be kept in a certain limit under all load conditions.

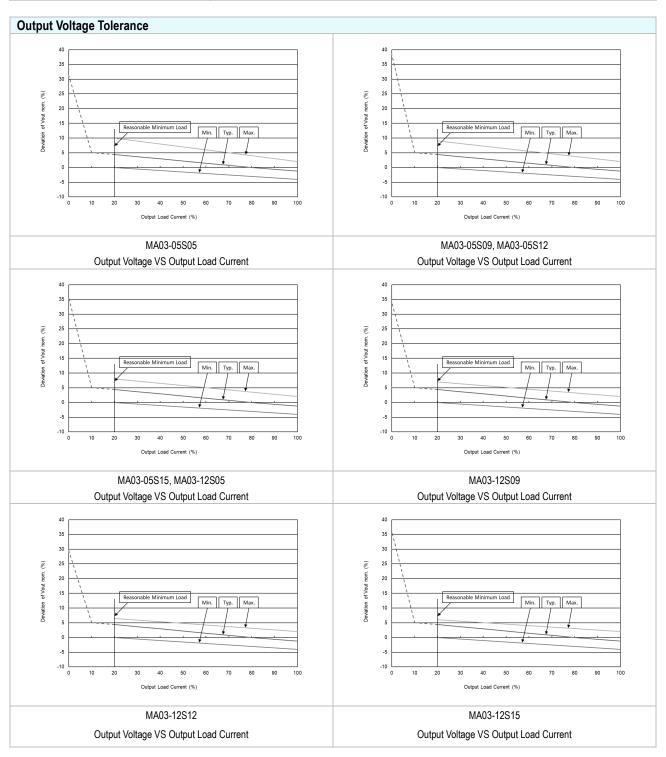
lodel Selecti	ion Guide										
Model	Input	Output	Output	Input		Load	Max. capacitive	Efficiency			
Number	Voltage Voltage Current Current	rent	Regulation	Load	(typ.)						
	(Range)		Max.	@Max. Load	@No Load			@Max. Load			
	VDC	VDC	mA	mA(typ.)	mA(typ.)	% (max.)	μF	%			
MA03-05S05		5	600	723		8	220	83			
MA03-05S09	5	9	333	689	50	7		87			
MA03-05S12	(4.5 ~ 5.5)	12	250	701	50	7		85.5			
MA03-05S15		15	200	686		6		87.5			
MA03-12S05		5	600	298	40	6	000	84			
MA03-12S09	12	9	333	285		5		87.5			
MA03-12S12	(10.8 ~ 13.2)	12	250	284		40	40	40	4.5	220	88
MA03-12S15		15	200	281		4		89			
MA03-24S05		5	600	152	30	5.8	8 220	82			
MA03-24S09	24	9	333	147		4.8		85			
MA03-24S12	(21.6 ~ 26.4)	12	250	146		4.3		85.5			
MA03-24S15		15	200	147		3.5		85			

Parameter	Model	Min.	Тур.	Max.	Unit	
Input Surge Voltage (1 sec. max.)	5V Input Models	-0.7		9	VDC	
	12V Input Models	-0.7		18		
	24V Input Models	-0.7		30		
	5V Input Models	4.5	5	5.5		
nput Voltage Range	12V Input Models	10.8	12	13.2		
	24V Input Models	21.6	24	26.4		
nput Filter	All Models	Internal Capacitor				



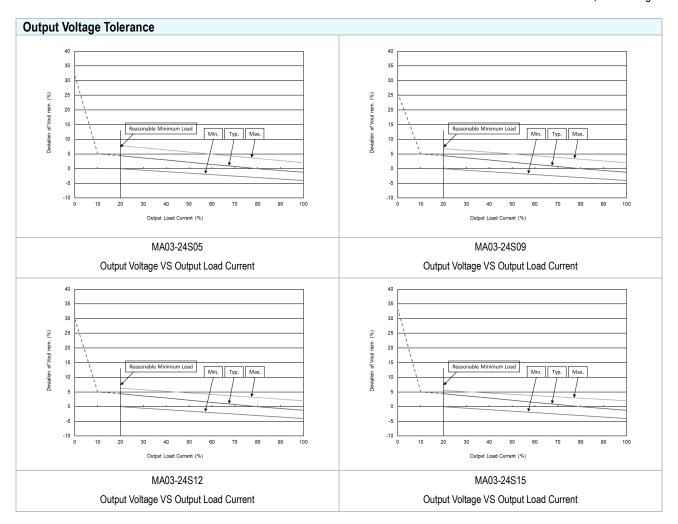


Output Specifications						
Parameter	Conditions	Min.	Тур.	Max.	Unit	
Line Regulation	For Vin Change of 1%		±1.01	±1.2	%	
			See Model Selection Guide			
Load Regulation	lo=20% to 100%	(Operation a	(Operation at lower load will not damage the converter,			
		but it may not		neet all specifications)		
Ripple & Noise	0-20 MHz Bandwidth			100	mV <sub>P-P</sub>	
Temperature Coefficient			±0.01	±0.02	%/°C	
Short Circuit Protection	0.5 Second Max., Automatic Recovery					



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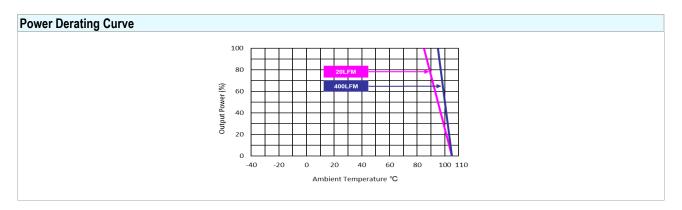




General Specifications						
Parameter	Conditions	Min.	Тур.	Max.	Unit	
WO. I. I. W. II.	60 Seconds	1000			VDC	
I/O Isolation Voltage	1 Second	1200			VDC	
I/O Isolation Resistance	500 VDC	1000			MΩ	
I/O Isolation Capacitance	100kHz, 1V		60	120	pF	
Switching Frequency			60		kHz	
MTBF (calculated)	MIL-HDBK-217F@25°C, Ground Benign	2,000,000 Hours			Hours	
	UL/cUL 60950-1 recognition(CSA certificate), IEC/EN 60950-1(CB-report)					
Safety Approvals	UL/cUL 62368-1 recognition(UL certificate), IEC/EN 62368-1(CB-report)					

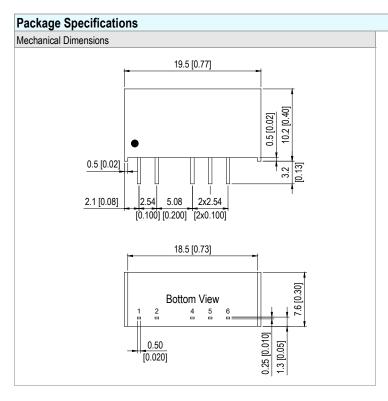
Environmental Specifications					
Parameter	Min.	Max.	Unit		
Operating Ambient Temperature Range (See Power Derating Curve)	-40	+85	°C		
Case Temperature		+100	°C		
Storage Temperature Range	-50	+125	°C		
Humidity (non condensing)		95	% rel. H		
Lead Temperature (1.5mm from case for 10Sec.)		260	℃		





#### Notes

- 1 Specifications typical at Ta=+25°C, resistive load, nominal input voltage and rated output current unless otherwise noted.
- 2 These power converters require a minimum output loading to maintain specified regulation, operation under no-load conditions will not damage these modules; however they may not meet all specifications listed.
- 3 We recommend to protect the converter by a slow blow fuse in the input supply line.
- 4 Other input and output voltage may be available, please contact MINMAX.
- 5 Specifications are subject to change without notice.
- The repeated high voltage isolation testing of the converter can degrade isolation capability, to a lesser or greater degree depending on materials, construction, environment and reflow solder process. Any material is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage. Furthermore, the high voltage isolation capability after reflow solder process should be evaluated as it is applied on system.



Pin Connections			
Pin	Function		
1	+Vin		
2	-Vin		
4	-Vout		
5	No Pin		
6	+Vout		

- ► All dimensions in mm (inches)
- ► Tolerance: X.X±0.25 (X.XX±0.01)

X.XX±0.13 (X.XXX±0.005)

► Pins ±0.05(±0.002)

# **Physical Characteristics**

Case Size	: 19.5x7.6x10.2mm (0.77x0.30x0.40 inches)	
Case Material	: Plastic resin (flammability to UL 94V-0 rated)	
Pin Material	: Alloy 42	
Weight	: 2.2g	

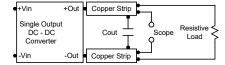
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### **Test Setup**

### Peak-to-Peak Output Noise Measurement Test

Use a Cout  $0.33\mu F$  ceramic capacitor. Scope measurement should be made by using a BNC socket, measurement bandwidth is 0-20 MHz. Position the load between 50 mm and 75 mm from the DC-DC Converter.



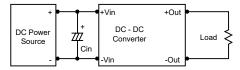
#### **Technical Notes**

### Maximum Capacitive Load

The MA03 series has limitation of maximum connected capacitance at the output. The power module may be operated in current limiting mode during start-up, affecting the ramp-up and the startup time. For optimum performance we recommend 220µF maximum capacitive load for devices. The maximum capacitance can be found in the data sheet.

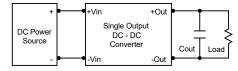
#### Input Source Impedance

The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. In applications where power is supplied over long lines and output loading is high, it may be necessary to use a capacitor at the input to ensure startup. Capacitor mounted close to the power module helps ensure stability of the unit, it is commended to use a good quality low Equivalent Series Resistance (ESR <  $1.0\Omega$  at  $1.00 \, \text{kHz}$ ) capacitor of a  $2.2 \, \mu\text{F}$  for the 5V input devices, a  $1.0 \, \mu\text{F}$  for the  $12 \, \text{V}$  input devices and a  $0.47 \, \mu\text{F}$  for the  $24 \, \text{V}$  devices.



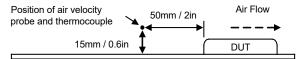
#### Output Ripple Reduction

A good quality low ESR capacitor placed as close as practicable across the load will give the best ripple and noise performance. To reduce output ripple, it is recommended to use  $1.0\mu$ F capacitors at the output.



#### **Thermal Considerations**

Many conditions affect the thermal performance of the power module, such as orientation, airflow over the module and board spacing. To avoid exceeding the maximum temperature rating of the components inside the power module, the case temperature must be kept below 100°C. The derating curves are determined from measurements obtained in a test setup.



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